L Number	Hits	Search Text	DB	Time stamp
4	14	(macro\$6 or cell) same library same gdsii	USPAT;	2004/08/02 16:23
	I		US-PGPUB;	
	I		EPO; JPO;	
	I		DERWENT;	
	I		IBM_TDB	
-	48754	("integrated circuit" or IC) same design\$3	USPAT	2004/07/22 16:32
	37081	(acertain\$3 or determin\$5) same function same specific\$5	USPAT	2004/07/22 16:32
-	5904	map\$5 same function same specific\$5	USPAT	2004/07/22 16:32
-	24354	(acertain\$3 or determin\$5) same function same (placement	USPAT	2004/07/22 16:32
	I	or location)		
-	80	(("integrated circuit" or IC) same design\$3) and ((acertain\$3	USPAT;	2004/07/22 16:33
	I	or determin\$5) same function same specific\$5) and (map\$5	US-PGPUB;	
	I	same function same specific\$5) and ((acertain\$3 or	EPO; JPO;	
	I	determin\$5) same function same (placement or location))	DERWENT;	
	I		IBM_TDB	
-	6317675	block based design	USPAT;	2004/07/22 16:33
	I		US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	163	block same design same placement same interconnection	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		(/black assess decises asses also assess to asses into assess at a second control of the control	IBM_TDB	2004/07/22 46-22
-	1	((block same design same placement same interconnection)	USPAT;	2004/07/22 16:33
	ļ	same function)and GDSII	US-PGPUB;	
	·		EPO; JPO; DERWENT;	
			IBM_TDB	
_	1	(block same design same placement same interconnection)	USPAT;	2004/07/22 16:33
	1	and GDSII	US-PGPUB;	2004/07/22 10.55
	I	und GDSII	EPO; JPO;	
	I		DERWENT;	
	I		IBM_TDB	
-	1619	block same design same placement	USPAT;	2004/07/22 16:33
	1		US-PGPUB;	
	1		EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	208526	layers same vias	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	80014	clock same power	USPAT;	2004/07/22 16:33
			US-PGPUB;	
]			EPO; JPO;	
			DERWENT;	
	434	lavera como electros acuar	IBM_TDB	2004/07/22 46 22
-	421	layers same clock same power	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
_	1432	clock same power same layer	IBM_TDB USPAT;	2004/07/22 16:33
-	1732	CIOCK SAITIE POWEL SAITIE IAYEL	US-PGPUB;	2007/07/22 10:33
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
			ם משובויום ב	<u> </u>

	42000	140 170 1 11 1- 1- 1	LICDAT	2004/07/22 46 22
-	13080	rout\$3 same different same (level or layer)	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1167	716/\$.ccls. and (block same size)	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
1			IBM_TDB	1
-	1529	716/\$.ccls. and (block same gate)	USPAT;	2004/07/22 16:33
		,, q ((J)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
	782	716/\$.ccls. and (block same transistor)	USPAT;	2004/07/22 16:33
-	/02	/10/\$.ccis. and (block same dansistor)		2004/07/22 10.33
			US-PGPUB;	
			EPO; JPO;	
	ŀ		DERWENT;	
		///!!	IBM_TDB	2004/07/20 : 5 5 5
-	22	((("integrated circuit" or IC) same design\$3) and	USPAT;	2004/07/22 16:33
	1	((acertain\$3 or determin\$5) same function same specific\$5)	US-PGPUB;	
	1	and (map\$5 same function same specific\$5) and	EPO; JPO;	
		((acertain\$3 or determin\$5) same function same (placement	DERWENT;	
		or location))) and 716/\$.ccls.	IBM_TDB	
-	51	"block based design"	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO, JPO;	
			DERWENT;	
			IBM_TDB	
1 -	5	"block based design" and (size same shape)	USPAT;	2004/07/22 16:33
1		Control of the contro	US-PGPUB;	
			EPO; JPO;	
Ì			DERWENT;	
			IBM_TDB	
_	2352	(716/\$.ccls. and (block same size)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:33
	2332	(block same gate)) or (716/\$.ccis. and (block same	US-PGPUB;	2007/07/22 10.33
		transistor))		
			EPO; JPO;	
	1		DERWENT;	
	1040	//71C/h asla and /block comp =!==\\ == /74C/h ==!= == 1	IBM_TDB	2004/07/22 46:22
_	1040	((716/\$.ccls. and (block same size)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:33
		(block same gate)) or (716/\$.ccls. and (block same	US-PGPUB;	
		transistor))) and hierarch\$6	EPO; JPO;	
			DERWENT;	
	1		IBM_TDB	
-	27	(block same design same placement same interconnection)	USPAT;	2004/07/22 16:33
	1	same function	US-PGPUB;	
	1		EPO; JPO;	
			DERWENT;	
]		IBM_TDB	
-	971	(((716/\$.ccls. and (block same size)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:34
		(block same gate)) or (716/\$.ccls. and (block same	US-PGPUB;	
	1	transistor))) and hierarch\$6) and function	EPO; JPO;	,
	1	,,,	DERWENT;	
	1		IBM_TDB	
-	1840	(716/\$.ccls. and (block same gate)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:34
	10.0	(block same transistor))	US-PGPUB;	
		(2.55) Salito d'alloistol //	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
			1011 100	l

-	267	GDSII	USPAT; US-PGPUB;	2004/07/22 16:34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	348	thousand adj gate	USPAT;	2004/07/22 16:34
			US-PGPUB;	, ,
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	79	(block same design same placement) and (layers same vias)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/07/00 46 04
-	10	(block same design same placement) and trailer	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
	1		DERWENT; IBM_TDB	
1.	496	small near5 function\$2 near5 block	USPAT;	2004/07/22 16:34
[056	Sman rical 3 function \$2 fical 3 block	US-PGPUB;	2007/0//22 10.34
			EPO; JPO;	
			DERWENT;	
	ļ		IBM_TDB	
_	597	small near5 function\$2 near5 (block or core)	USPAT;	2004/07/22 16:34
		(US-PGPUB;	,,
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	7901	716/\$.ccls. (small near5 function\$2 near5 block)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		746/4	IBM_TDB	2004/07/22 46:24
-	75	716/\$.ccls. and (small near5 function\$2 near5 (block or	USPAT;	2004/07/22 16:34
		core))	US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
_	65	(block same design same placement) and (glue near3 logic)	USPAT;	2004/07/22 16:34
		(2.2.2. Same assign same placement) and (glac hears logic)	US-PGPUB;	
	1		EPO; JPO;	
			DERWENT;	
	1		IBM_TDB	
-	16	(block same design same placement) and (layers same clock	USPAT;	2004/07/22 16:35
		same power)	US-PGPUB;	
	1		EPO; JPO;	
1	1		DERWENT;	
1		(deleterance conservations)	IBM_TDB	2004/07/22 46 24
-	36	(clock same power same layer) and (block same design	USPAT;	2004/07/22 16:34
	1	same placement)	US-PGPUB;	
			EPO; JPO; DERWENT;	
	1 .		IBM_TDB	
_	1472	glue adj logic	USPAT;	2004/07/22 16:34
	11/2	3.10 1, 10g10	US-PGPUB;	
			EPO; JPO;	
]		DERWENT;	
			IBM_TDB	

-	2094	(power same layer) and (clock same layer)	USPAT;	2004/07/22 16:34
1			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	2517	(power same layer) and (clock same layer)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1432	(power same layer) same (clock same layer)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	156	(block same design same placement) and (rout\$3 same	USPAT;	2004/07/22 16:34
	150	different same (level or layer))	US-PGPUB;	2001/07/22 10:51
		different sum (level of layer))	EPO; JPO;	
			DERWENT;	
	89	((nower came layer) and (clock came layer)) and 716/4 cale	IBM_TDB	2004/07/22 16:34
-	09	((power same layer) and (clock same layer)) and 716/\$.ccls.	USPAT;	2004/07/22 10:34
			US-PGPUB;	
ļ			EPO; JPO;	
			DERWENT;	
		00/000 500	IBM_TDB	
-	3	09/098,599	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	716/\$.ccls. and ("data path" same memory same control	USPAT;	2004/07/22 16:34
	İ	same i/o same analog)	US-PGPUB;	
			EPO; JPO;	
	ľ		DERWENT;	
			IBM_TDB	
-	1	716/\$.ccls. and ("data path" same memory same control	USPAT;	2004/07/22 16:34
		same i/o same analog same group)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	87	"1000 gates"	USPAT;	2004/07/22 16:34
		5	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
l -	62	((((716/\$.ccls. and (block same size)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:34
		(((/16/5.ccis. and (block same size)) or (716/\$.ccis. and (block same	US-PGPUB;	230 1/07/22 10.37
		transistor))) and hierarch\$6) and function) and(trailer or	EPO; JPO;	
	1	"glue logic")	DERWENT;	
	1	gide logic /		
l <u> </u>	68	((716/\$.ccls. and (block same gate)) or (716/\$.ccls. and	IBM_TDB	2004/07/22 16:24
-	68		USPAT;	2004/07/22 16:34
	İ	(block same transistor))) and (trailer or "glue logic")	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	110	74.5 (A. and a sub-fall of	IBM_TDB	2004/07/22 15 21
-	113	716/\$.ccls. and sub-block	USPAT;	2004/07/22 16:34
			US-PGPUB;	
1]		EPO; JPO;	
			DERWENT;	
			IBM_TDB	

-	65	(thousand adj gate) same block	USPAT; US-PGPUB;	2004/07/22 16:34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	39	716/\$.ccls. and ((thousand adj gate) same block)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	8	716/8.ccls. and (thousand adj gate)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	77	716/\$.ccls. and (thousand adj gate)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
_	71	716/\$.ccls. and (small near5 function\$2 near5 block)	IBM_TDB	2004/07/22 16:24
-	/1	(/ 10/p.ccis. and (smail near) infictions near)	USPAT; US-PGPUB;	2004/07/22 16:34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	87	(glue adj logic) and 716/\$.ccls.	USPAT;	2004/07/22 16:34
]	(glue adj logic) and 710/\$.ccis.	US-PGPUB;	2001/07/22 10.51
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	"5859999".PN.	USPAT	2004/07/22 16:34
-	76	((power same layer) same (clock same layer)) and	USPAT;	2004/07/22 16:35
		716/\$.ccls.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	"6014507".PN.	USPAT	2004/07/22 16:35
-	1	"5984510".PN.	USPAT	2004/07/22 16:35
-	1	"5860017".PN.	USPAT	2004/07/22 16:35
-	1 22222	"5841664".PN.	USPAT	2004/07/22 16:35
-	22883	(block or group or macro\$5) same library	USPAT;	2004/07/22 16:37
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
1_	1261	((block or group or macro\$5) same library) and 716/\$.ccls.	USPAT;	2004/07/22 16:37
	1201	((block of group of macross) sume library) and 710/\$.ccis.	US-PGPUB;	2001/07/22 10.57
	1		EPO; JPO;	
	ļ		DERWENT;	
	1		IBM_TDB	
-	660	(block or group or macro\$5) same library same layers	USPAT;	2004/07/22 16:37
			US-PGPUB;	, ,======
	1		EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	100	((block or group or macro\$5) same library same layers) and	USPAT;	2004/07/29 14:33
		716/\$.ccls.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
L		<u> </u>	IBM_TDB	

-	81	((block or macro\$5) same library same layers) and	USPAT;	2004/07/29 14:34
		716/\$.ccls.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	30	((block or macro\$5) same library same layers) and	USPAT;	2004/07/29 14:35
		716/\$.ccls.	US-PGPUB;	
İ		7 2 9, 41 3 3 3 3	EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
	33	(/block or macrotE) came librart4 came layers) and	USPAT;	2004/07/29 14:35
_	33	((block or macro\$5) same librar\$4 same layers) and		2004/07/29 14.33
		716/\$.ccls.	US-PGPUB;	
			EPO; JPO;	
1			DERWENT;	
			IBM_TDB	
-	14	(macro\$5 same librar\$4 same layers) and 716/\$.ccls.	USPAT;	2004/07/29 14:41
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	:
l <u>-</u>	72	(macro\$5 same (wir\$5 or intercon\$8) same layers) and	USPAT;	2004/07/29 15:49
	/2	716/\$.ccls.	US-PGPUB;	200 1/07/25 15.15
		710/\$.ccis.		
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	macro\$5 same netlist same layout same pin same speed	USPAT;	2004/07/29 15:52
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	59	macro\$5 same netlist same layout	USPAT;	2004/07/29 15:54
1		,,,,,,	US-PGPUB;	, , ,
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	i
	9	macrodE came notlict came layout came hard	USPAT;	2004/07/20 16:29
-	9	macro\$5 same netlist same layout same hard		2004/07/29 16:38
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	"5883814".PN.	USPAT	2004/07/29 15:57
-	25	cell same netlist same layout same hard	USPAT;	2004/07/29 16:39
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
1 -	13	cell same netlist same layout same hard same (connec\$5 or	USPAT;	2004/08/02 14:47
		pin)	US-PGPUB;	
		F"'''	EPO; JPO;	
			DERWENT;	
		"E000074"	IBM_TDB	2004/00/02 44:53
-	17	"5880971"	USPAT;	2004/08/02 14:52
			US-PGPUB;	
			EPO; JPO;	
]		DERWENT;	
			IBM_TDB	
-	0	"seemingly insurmountable job of designing a highly	USPAT;	2004/08/02 14:53
		complex circuit is broken into small, workable design	US-PGPUB;	
		projects"	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
L			1 101 100	L

-	3	"seemingly insurmountable job"	USPAT;	2004/08/02 16:00
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	109	(macro\$6 or cell) same library same different same size	USPAT;	2004/08/02 16:02
		same shape	US-PGPUB;	
	1		EPO; JPO;	
			DERWENT;	
	ļ		IBM_TDB	
-	0	(((macro\$6 or cell) same library same different same size	USPAT;	2004/08/02 16:04
		same shape) and 716/\$.ccls.) and gdsiii	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	GDSIII	USPAT;	2004/08/02 16:03
		,	US-PGPUB;	
			EPO; JPO;	
	1		DERWENT;	
			IBM_TDB	
-	270	GDSII	USPAT;	2004/08/02 16:03
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	(((macro\$6 or cell) same library same different same size	USPAT;	2004/08/02 16:23
		same shape) and 716/\$.ccls.) and gdsii	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	65	((macro\$6 or cell) same library same different same size	USPAT;	2004/08/02 16:05
		same shape) and 716/\$.ccls.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		·	IBM TDB	